

	I/O Lines	Load on...	Input Voltage Range	Output Voltage Range	Output Current per Channel	Front Connector	Isolation	Consumption typ.	Software
M82	16 inputs		0 V to 40 V			44-pin HD-Sub	Yes	220 mA	Windows, Linux, QNX, OS-9, VxWorks
M81	16 outputs			0 V to 36 V	500 mA	44-pin HD-Sub	Yes	330 mA (max.)	Windows, Linux, QNX, OS-9, VxWorks
M66	32 inputs or outputs	Ground	0 V to 32 V	12 V to 32 V	1.9 A max.	44-pin HD-Sub	Yes	200 mA	Windows, Linux, QNX, OS-9, VxWorks
M58	32-bit TTL I/O with line termination					44-pin HD-Sub		200 mA	Windows, Linux, QNX, OS-9, VxWorks
M43N	8 relay outputs			48 V max.	1 A	25-pin D-Sub	Yes	3.5 W	Windows, Linux, QNX, OS-9, VxWorks
M32	16 inputs	Supply voltage	0 V to 180 V			25-pin D-Sub	Yes	50 mA	Windows, Linux, QNX, OS-9, VxWorks
M31	16 inputs	Ground	0 V to 180 V			25-pin D-Sub	Yes	50 mA	Windows, Linux, QNX, OS-9, VxWorks
M28	16 outputs	Ground		8 V to 36 V	500 mA	25-pin D-Sub	Yes	100 mA	Windows, Linux, QNX, OS-9, VxWorks
M27	16 outputs	Supply voltage		8 V to 36 V	500 mA	25-pin D-Sub	Yes	100 mA	Windows, Linux, QNX, OS-9, VxWorks
M11	18-bit TTL I/O					25-pin D-Sub		200 mA	Windows, Linux, QNX, OS-9, VxWorks
M199	FPGA/Nios-based main M-Module with 32 MB SDRAM and 8 MB Flash for integration of user-defined I/O on a USM Universal Submodule plugged onto the main module					1x 50-pin SCSI	Depending on USM		Depending on IP core functions