

# XMC and PMC Modules



## XMC

	Function	Intelligence	I/O Lines	Memory	Miscellaneous	Front Connector	Isolation	Consumption typ.	Software
<b>P602</b>	Quad Redundant Gigabit Ethernet		4 channels, 10/100/1000Base-T		2 XMC connectors with 1 x4 PCIe link on each connector	4x RJ45	Yes	+3.3 V: 100 mA +5 V: 1.4 A +12 V: 600 mA	Windows, Linux
<b>P601</b>	Quad Redundant Gigabit Ethernet		4 channels, 10/100/1000Base-T		1 XMC connector with 2 x4 PCIe links	4x RJ45	Yes	+3.3 V: 100 mA +5 V: 1.4 A +12 V: 600 mA	Windows, Linux

## PMC

	Function	Intelligence	I/O Lines	Memory	Miscellaneous	Front Connector	Isolation	Consumption typ.	Software
<b>P599</b>	Main module for integration of user-defined I/O	FPGA/Nios	Depending on IP core and USM configuration	32 MB SDRAM 2 MB Flash	Based on USM Universal Submodule concept	1x 50-pin SCSI	Depending on USM	+3.3 V: 82 mA +5 V: 109 mA	Depending on IP core functions
<b>P598</b>	Conduction-cooled main module for integration of user-defined I/O	FPGA/Nios	Depending on IP core and USM configuration	32 MB SDRAM 2 MB Flash	Based on USM Universal Submodule concept		Depending on USM	+3.3 V: 76 mA +5 V: 118 mA	Depending on IP core functions
<b>P512</b>	Reflective Memory		1 LVDS channel (TX/RX)	32 MB SDRAM	Usable in fully connected mesh	1x 50-pin SCSI	No	+3.3 V: 143 mA +5 V: 109 mA	Windows, Linux, VxWorks, QNX
<b>P511</b>	Dual Fast Ethernet	FPGA (Nios optional)	2 channels, 10/100Base-T	32 MB SDRAM 2 MB Flash	Based on USM Universal Submodules	2x RJ45 cable to 50-pin SCSI	1500 VAC	+3.3 V: 139 mA +5 V: 40 mA	Windows, Linux
<b>P506</b>	Quad CAN bus	FPGA (Nios optional)	4 independent channels CAN 2.0A/B	32 MB SDRAM 2 MB Flash	Based on USM Universal Submodules	1x 50-pin SCSI	1500 VAC	+3.3 V: 15 mA +5 V: 240 mA	Windows, Linux